

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Anton Rozen, et al. GROUP ART UNIT: 4154
APPLN. NO.: 10/595,908 EXAMINER: Hernandez, Manuel J
FILED: August 22, 2006
TITLE: METHOD AND DEVICE FOR REGULATING A VOLTAGE
SUPPLY TO A SEMICONDUCTOR DEVICE

Certificate of Submission

I hereby certify that this correspondence is being submitted to the USPTO, Alexandria, VA.

- ☐ Addressed per C.F.R. § 1.1(a) and deposited with the United States Postal Service with sufficient postage as first class mail.
- ☐ Facsimile transmitted in accordance with C.F.R. § 1.6(d).
- ☒ Submitted electronically via EFS in accordance with "Legal Framework for EFS Web".

December 14, 2009
Date of Submission

/Dee Matocha/
Signature

Dee Matocha
Printed Name of Person Signing Certificate

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please enter the following in response to the Office Action dated 10/29/09 in the above-entitled application, without prejudice or disclaimer.

If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit Account 503079, Freescale Semiconductor, Inc.